



(Use as many sheets as necessary)

Sheet	one	of	two
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Complete if Known

Application Number	10/658,506
Filing Date	September 9, 2003
First Named Inventor	Zheng et al.
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	AF01182

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Cash Phung

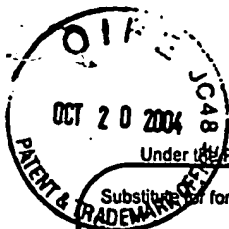
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PTO/SB/088 (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number 10/658,506

Filing Date September 9, 2003

First Named Inventor Zheng et al.

Art Unit 2818

Examiner Name Unknown

Sheet two

of two

Attorney Docket Number AF01182

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ap		Reisinger, H. et al., "A Novel SONOS Structure for Nonvolatile Memories with Improved Data Retention," 1997 Symposium on VLSI Technology Digest of Technical Papers, June 10, 1997, pages 113-114.	

Examiner
Signature*Anh Phung*Date
Considered

4/27/05

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Form PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Atty Docket No. AF01182	Serial No. 10/658,506
	Applicant: Zheng et al.	
	Filing Date September 9, 2003	Group 2818

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date (MM/YYYY)	Name	Class	Sub-class	Filing Date if Appropriate
CP	6,215,702	04/2001	Derhacopian et al.			
CP	6,137,718	10/2000	Reisinger			
CP	5,311,049	05/1994	Tsuruta			
CP	6,265,268	07/2001	Halliyal et al.			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date (MM/YYYY)	Country	Class	Sub-class	Translation	
						Yes	No
CP	WO 98/06138	02/1998	WO				X

OTHER ART

Examiner Initial	Author, Title, Date, Pertinent Pages, etc.
CP	Reisinger H. et al., "A Novel Sonos Structure for Nonvolatile Memories with Improved Data Retention", 1997 Symposium on VLSI Technology Digest of Technical Papers, June 10, 1997, pages 113-114

EXAMINER <i>Chieh Phung</i>	DATE CONSIDERED 4/27/05
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Information Disclosure Statement PTO-1449 (Modified)

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